Sheet	1	of	1
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Figure 16-	1449	C	OM	1Ml	ERG	CE	PA	ENT OF TENT AND	ATTY DOCKET NO 0325.00459	Э.	SERIAL NO. 09/823,414			
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-	_							U.	S. PATENT DOCUM	ENTS	<u>-</u> "	nter	2001	Æ
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Vn	4	6	8	9	7	4	0	8/25/1987	Moelands et al.		364	200		
1	5	5	5	9	5	0	2	9/24/1996	Schutte		340	825.21		
	5	6	8	9	1	9	6	11/18/1997	Schutte		326	86		
	5	9	7	4	4	8	6	10/26/1999	Siddappa		710	53		
	6	0	1	2	1	0	3	1/4/2000	Sartore et al.		710	8		
	6	1	4	5	0	4	5	11/7/2000,	Falik et al.		710	129		
	6	1	4	8	3	5	4	11/14/2000	Ban et al.		710	102		
$\sqrt{\lambda}$	6	1	7	3	3	5	5	1/9/2001	Falik et al.		710	129		
								FORE	EIGN PATENT DOC	UMENTS				
-	Document No. Date				Date	Country	Class	Sub-	Transl.					
												Class	Yes	No
WO 96	_	1	1	7	3	0	5	06.06.96	PCT		G06F		Х	
WO 99		1	0	9	7	1	2	25.02.99	PCT		Н04	,	Х	
WO 98		1	3	4	3	7	6	06.08.98	PCT		H04L	<u> </u>	Х	
					OT	H	ER	DOCUMENTS	(Incl. Author, Title,	Date, Pertinent	Pages, etc.)			
Vn									ED CIRCUIT AND MI ERSAL SERIAL BUS					
m		_							. INTERFACE ENGIN RS", Cypress Press Re			ECTIVITY	OT Y	
vaj		"ISP1581 UNIVERSAL SERIAL BUS 2.0 HIGH-SPEED INTERFACE DEVICE", October 23, 2000, Rev. 02, pp. 1-73.												
w	"("CY7C68001 EZ-USB SX2 HIGH-SPEED USB INTERFACE DEVICE", January 4, 2001, pp. 1-29.												
un	j"["UNIVERSAL SERIAL BUS SPECIFICATION", Revision 1.1, September 23, 1998, pp. 1-311.												
W	J"	JNI	VE	RS.	AL	SE	RIA	AL BUS SPECI	FICATION", Revision	2.0, April 27, 20	000, pp. 1-622.			
Examiner	W	1	n	<u></u>)	D	2	*	Date Considered	10/20/03				
Examiner Examiner: I	<i>lu</i>	1 if	ref	erer	nces	s cc	onsi	dered, whether	·	10/20/03 nformance with M	1PEP 609. Dra		ough c	_ _ :it